

Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1 – 16. (Cancelled).

17. (Currently amended) An electronic circuit comprising conversion means for converting an input voltage into an output voltage, ~~and said~~ conversion means comprising:

at least a first energy storage means and a second energy storage means; and
switching means for periodically coupling said at least first and second energy storage means to one another under the control of clock signals so as to store energy in the at least first and second energy storage means, and for transferring at least a portion of the stored energies between the at least first and second energy storage means; and

clock signal generating means for generating the clock signals, ~~said control-clock~~ signal generating means keeping the clock signals in holding states during a holding period during operation, said holding states being equal to the states of the respective clock signals immediately before the holding state.

18. (Currently amended) The electronic circuit as claimed in claim 17, ~~characterized in that~~ wherein the switching means and the at least first and second energy storage means are implemented with the use of at least one charge pump.

19. (Currently amended) A medium for ~~storage~~storing/reading of user information, comprising an integrated circuit comprising the electronic circuit as defined in claim 17.

20. (Currently amended) The medium as claimed in claim 19, ~~characterized in that~~wherein the integrated circuit comprises a photosensitive sensor for providing the input voltage ~~[[t]]~~ when the photosensitive sensor receives a substantial quantity of light.

21. (Currently amended) The medium as claimed in claim 20, ~~characterized in that~~wherein the integrated circuit furthermore comprises memory means provided with a supply voltage through utilization of the output voltage.

22. (Currently amended) The medium as claimed in claim 21, ~~characterized in that~~wherein the integrated circuit further comprises a microprocessor and a further photosensitive sensor for providing additional information to the microprocessor, said microprocessor processing the additional information, and said microprocessor being coupled to the memory means for storing the processed additional information.

23. (Currently amended) The medium as claimed in claim 21, ~~characterized in that~~wherein the integrated circuit further comprises a microprocessor and a further photosensitive sensor for providing additional information to the memory means for storing the additional information, the microprocessor being coupled to the memory means for processing the additional information after reading of the additional information from the memory means.

24. (Currently amended) The medium as claimed in claim 22, ~~characterized in that~~wherein the length of the holding period corresponds by approximation to that of a time period during which the photosensitive sensor does not receive a substantial quantity of light to provide the input voltage.

25. (Currently amended) The medium as claimed in claim 24, ~~characterized in that~~wherein the microprocessor is idle during the holding period, and ~~in that~~ the integrated circuit further comprises a standby circuit for supplying the microprocessor with a supply voltage during the holding period ~~(RT)~~.

26. (Currently amended) The medium as claimed in claim 19, ~~characterized in that~~wherein the medium is an optical disc having a side for storing and reading the user information, wherein the integrated circuit is fastened to said side of the optical disc in a region not reserved for storing and reading of the user information.

27. (Currently amended) The medium as claimed in claim 19, ~~characterized in that~~wherein the medium is an optical disc having a first side for storing and reading of the user information, wherein the integrated circuit is fastened to a second side of the optical disc.

28. (Currently amended) A recording/playback device comprising means for storagestoring/reading of information onto/from the medium as claimed in claim 19.